

Docket No.: MUH-12728

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By: 

Date: August 20, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hans Weber et al.
Applic. No. : 10/631,350
Filed : July 31, 2003
Title : Method for Forming a Channel Zone of a Transistor and PMOS Transistor

INFORMATION DISCLOSURE STATEMENT

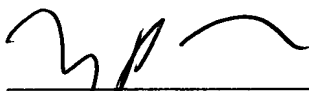
Hon. Commissioner for Patents

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

Bobby J. Daniel et al.: "Modeling of the CoolMOSTM Transistor – Part I: Device Physics", *IEEE Transactions on Electron Devices*, Vol. 49, No. 5, May 2002, pp. 916-920.

Respectfully submitted,



For Applicants

Mark P. Weichselbaum
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Date: August 20, 2003

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FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))	Attorney Docket No.: MUH-12728 Applicant Hans Weber et al. Filing Date July 31, 2003 Applic. No. 10/631,350 Group Art Unit
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U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J						
	K						
	L						
	M						
	N						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	O	Bobby J. Daniel et al.: "Modeling of the CoolMOS™ Transistor – Part I: Device Physics", <i>IEEE Transactions on Electron Devices</i> , Vol. 49, No. 5, May 2002, pp. 916-920
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EXAMINER	DATE CONSIDERED
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	